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APPLICATION FOR LETTERS PATENT

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FOR

ENGINEERING OPERATIONS CHANNEL PROVISION

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ENGINEERING OPERATIONS CHANNEL PROVISION

This invention relates to arrangements and methods for providing an engineering operations channel (EOC) in association with digital services delivered to a user terminal.

5 BACKGROUND OF THE INVENTION

In a conventional telephone system, the majority of subscribers are connected to local exchanges via twisted conductor pairs, generally referred to as subscriber loops. Between the subscribers and the exchange, the subscriber loops are carried in cables each containing a large number of conductor pairs. The cables from the exchange feed smaller street cables from which the individual subscriber loops are 'dropped' to provide the final link to the subscriber. These twisted pair subscriber loops, which were originally installed to carry voice services, are now being used by the system operators to carry new services such as ADSL (asymmetric digital subscriber line) in the frequency spectrum above the base band frequencies used for the voice or POTS traffic. These services are added to the subscriber line at the exchange and may carry e.g. video and/or Internet traffic to the subscriber.

The currently employed ADSL service is a broad band technology which occupies a frequency band above that of voice to provide high bit rate (asymmetric) services to customers. Typically this service uses discrete multi-tone technology (DMT) at frequencies of about 20 to 100kHz up to about 1.1 MHz at a downstream bit rate of 2Mb/s and at an upstream bit rate of 200 kb/s. The ADSL service is usually delivered via a carrier based system using discrete multi-tone (DMT) in which the traffic is

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carried on a number of frequencies over the twisted pair to the subscriber.

There are now proposals to introduce a new higher bit rate interactive or symmetric service generally referred to as VDSL (very high bit rate digital subscriber line). This service is intended primarily for business customers to carry large volumes of data traffic to and from such customers. For most users the service will be provided over an existing twisted pair subscriber loop or link coupling that user's terminal to a local exchange so as to avoid the cost of installing e.g. a coaxial link to the subscriber.

A particular problem with the introduction of VDSL systems is that of managing and controlling the subscriber link to allow remote status and performance monitoring of the link and to perform management operations which require downloading of information and commands. A further requirement is that of providing synchronisation between the transmitters and receivers at the exchange and subscriber ends of the links. This synchronisation of transmitter and receiver is essential where the digital data transmitted over the link has been scrambled to provide an even distribution of binary ones and zeros.

In a conventional e.g. ADSL system, an engineering operations channel is provided by the allocation of bits, often byte oriented, in each superframe structure of a transmission line-code. This provides the transport of a set of so-called indicator bits and control messages.

Currently employed EOC arrangements suffer from the disadvantage that they are protocol specific and thus lack flexibility. In an attempt to reduce this problem, there have been proposals for inserting EOC information into an ATM channel. This has the advantage of common cell

processing, but the need to accommodate fixed length cells has limited the efficiency of the technique.

SUMMARY OF THE INVENTION

5 An object of the invention is to minimise or to overcome the above disadvantage.

A further object of the invention is to provide an improved engineering operations channel arrangement and method for use in a VDSL system.

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A further object of the invention is to provide an improved method of managing a subscriber link between a VDSL customer and an exchange.

A further object of the invention is to provide an improved digital communication service.

According to a first aspect of the invention there is provided a method of transporting digital traffic over a line from a central station to a subscriber terminal, the method comprising providing an engineering operations channel over the line, wherein said engineering operations channel is transported in asynchronous minicells.

According to a further aspect of the invention there is provided a method of providing a digital communication service over a line from a line termination equipment disposed at a central station to a subscriber terminal, the method comprising providing a engineering operations channel for effecting control and management of the subscriber terminal, and transporting said engineering operations channel in a sequence of asynchronous minicells over the line.

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According to a further aspect of the invention there is provided apparatus for providing a digital communication service over a line from a line

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termination equipment disposed at a central station to a subscriber terminal, wherein the line termination equipment and the subscriber terminal incorporate respective first and second management systems arranged to control and supervise said digital communication service via messaging therebetween carried in an engineering operations channel over the line, and wherein the line termination equipment and the subscriber terminal incorporate means for providing said engineering operations channel in the form of a sequence of asynchronous minicells over the line.

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According to a further aspect of the invention there is provided a digital communications system, comprising a subscriber network termination, a line termination equipment, and a transmission path therebetween, the subscriber termination and the line termination being coupled to the path via respective first and second modems, wherein the subscriber termination and the line termination each incorporate respectively a first and second management system each system consisting of a corresponding plurality of management levels, said first and second management systems being arranged to control and supervise said digital communication service via messaging carried in an engineering operations channel over the line, wherein said subscriber termination and the line termination each incorporate respective multiplexer means interfacing with the management levels of that termination, and wherein said subscriber termination and line termination incorporate respective packet transaction means each interfacing with the respective multiplexer means for carrying messaging between corresponding subscriber termination and line termination management levels in an engineering operations channel over the line, said engineering operations channel being comprised by a sequence of asynchronous minicells over the line.

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According to a further aspect of the invention there is provided a method of controlling digital communications system comprising a subscriber

network termination, a line termination equipment, and a transmission path therebetween, the subscriber termination and the line termination each incorporating respectively a first and second management system each system consisting of a corresponding plurality of management levels, said first and second management systems being arranged to control and supervise said digital communication service, the method messaging paths between corresponding comprising providing management levels, and multiplexing said messaging paths into an engineering operations channel over the line, and wherein said engineering operations channel is transported in a sequence of asynchronous minicells over the line.

In a further aspect the invention provides a digital subscriber network termination for receiving a digital service over a subscriber line coupled thereto, the subscriber termination including a management system consisting of a plurality of management levels, said first and second management system being arranged to control and supervise said digital communication service via messaging carried in an engineering operations channel over the line, multiplexer means interfacing with the management levels of the subscriber termination, and packet transaction means interfacing with the multiplexer means for carrying messaging to and from the management levels in an engineering operations channel over the line, said engineering operations channel being comprised by a sequence of asynchronous minicells over the line.

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The technique provides the advantage of cell based transport without the constraint of fixed length cells and their associated assembly delay. Further, by carrying the EOC channel in minicells, this channel is rendered wholly independent of the protocol or protocols employed for VDSL transport.

In a preferred embodiment, spare capacity in the EOC channel may be used to accommodate low delay telephony traffic e.g. to provide a voice over Internet service.

The minicells transporting the EOC channel may be of variable length, the start of each cell being determined by the use of pointers and/or a length indicator (LI) field.

BRIEF DESCRIPTION OF THE DRAWINGS

10 Embodiments of the invention will now be described with reference to the accompanying drawings in which:-

Figure 1 is a schematic diagram showing a VDSL subscriber terminal coupled to a local exchange via a subscriber link;

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Figure 2 shows the exchange and subscriber terminal equipment used in the arrangement of figure 1;

Figure 3 illustrates a first method of providing an engineering operations channel over the subscriber link of figure 1;

Figure 4 illustrates a modification of the EOC provision of figure 3;

Figure 5 shows a register access messaging scheme for use with the EOC provision of figure 4;

Figures 6a and 6b illustrate a service specific common sublayer (SSCS) for synchronous and plesiochronous traffic respectively;

Figure 7 illustrates a method of scrambler synchronisation in the arrangement of figures 1 and 2; and

Figure 8 illustrates a descrambler synchronisation arrangement for use with the scrambling process of figure 7.

5 DESCRIPTION OF PREFERRED EMBODIMENTS

Referring first to figure 1, this illustrates in schematic form the connection of a VDSL user terminal 11 to an exchange, generally indicated as 12, via a subscriber loop 13. Typically, the subscriber loop 13 will be terminated on a main distribution frame (MDF) 14 at the exchange and will be coupled thereby to a VDSL line card 15. The line card 15 is in turn coupled to a switch (not shown) whereby VDSL traffic may be routed to and from the subscriber terminal 11. Typically the VDSL traffic over the subscriber loop in both the upstream and downstream directions is carried in a frequency band above 1.2 MHz.

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Figure 2 shows the exchange or line terminating equipment (LTE) 21 and the subscriber or network termination (NT) 22 that may be employed in the provision of the VDSL service to the subscriber. The LTE 21 and the NT 22 are each coupled to the subscriber line 13 via a respective modem 210 and 220.

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The line terminating equipment performs the function of a line card for the VDSL service and is coupled to an ATM backplane 211 via an interface 212, a transaction protocol specific (TPS) transaction conveyance (TC) function 213 and a packet mode data (PMD) transaction conveyance (TC) function 214. It will be understood that a number of similar line termination equipments, one for each VDSL subscriber, will be coupled to the backplane 211. The network termination modem 220 is similarly coupled to the user network Interface (UNI) via a transaction protocol specific (TPS) transaction conveyance (TC) function 223 and a packet mode data (PMD) transaction conveyance (TC) function 224.

Control of the line terminating equipment and the network terminating equipment is provided by a number of management layers. At the lowest level, a physical performance monitoring and management layer 216, 226 monitors modern performance and provides a transmission convergence functionality. This layer is independent of transmission protocols. The next level is a transport protocol specific (TPS) transaction conveyance management layer convergence layer 217, 227. This layer is associated with ATM protocols, management and performance monitoring. The highest level comprises a transport protocol specific (TPS) management layer 218,228 providing e.g. traffic Communication between the shaping and system level functions. corresponding management layers in the line termination and the network termination is effected via AAL2 multiplexers 219, 229, this management communication traffic being carried in the EOC channel over the subscriber line. Optionally, this management traffic may be carried in AAL2 minichannels. Indicator bits relating to the PHY performance monitoring and management may be sent in AAL2 packets or may be sent by direct EOC multiplexing.

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The network termination incorporates a further NT management level 230 that communicates with a remote network manager 231 via a further AAL2 multiplexer 232 and an ATM interface 233 disposed in the line termination equipment 21.

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In the arrangement of figures 1 and 2, traffic is transported over the subscriber loop in asynchronous minicells. The engineering operations channel (EOC) is framed and byte oriented, one EOC frame being equivalent to one line-code super-frame, the frame structure being illustrated in figure 3. The EOC channel contains a number information elements, these principally comprising a super-frame synchronisation pattern, generic indicator bits, transport protocol indicator bits and EOC

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messages. The format of the EOC messages is that of the AAL2 common part sub-layer packets defined in the ITU draft recommendation I363.2 the content of which is incorporated herein by reference. The EOC message format further incorporates a special service specific convergence sub-layer defined for VDSL physical layer EOC applications.

The first byte of the EOC message field comprises a six bit pointer, a sequence number bit and parity bits. The pointer identifies the start of the first valid mini-cell in the EOC frame. The EOC message field is followed by minicells which are packed into ATM cells as defined by the AAL2 protocol.

In a modification of the technique illustrated in Figure 4, the EOC synchronisation pattern is provided by the header of an AAL2 common port sublayer (CPS) packet with a special channel identifier (CID) which contains as payload the indicator bits as defined above. In this embodiment, delineation of super-frame boundaries is based on the known super-frame length and consequently on the known periodic arrival of valid CPS-packet header error control (HEC) bits. Initial delineation may be implied by the link start-up process, or may be subsequently re-established by hunting for a number of correct HEC determinations and detection of the unique CID on predicted superframe boundaries. Further, loss of super-frame delineation may be detected by detection of a predetermined number of HEC failures in a given period. Control of the super-frame delineation process can be modelled on the state-machine used for ATM cell delineation.

The indicator bits are packed into a number of bytes of synchronisation packet payload or, if 5 bits are sufficient, can be mapped onto the AAL2 user to user information (UUI) field for this specific CID and are consequently protected by the HEC. Where carried in the payload, an

additional checksum/parity byte may be added to protect the payload, or one or more bits of the UUI may be allocated as parity bits to provide the same function.

Any number of AAL2 CPS-packets may be included in a superframe, but padding bytes are inserted to ensure that superframe header synchronisation. Optionally network timing reference phase information can be carried in he mini-cell where the transport system clock is not synchronised to an NTR.

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Where the user terminal is provided with a register, this may be used to store command and control information. Access to the register may be effected via the messaging scheme illustrated in figure 5. The scheme includes register access messages to write information into and to read information from the register, and acknowledgement messages which confirm to the exchange that a read and/or write instruction has been performed. The messaging scheme may also accommodate simple command messages that require no acknowledgement, e.g. a message reporting an imminent shut-down of transmission, by the use of a short message field. A further short message field provides for the insertion of indicators as required.

EOC messages can be directed to the appropriate remote functional entity by using the mini-cell CID as a discriminator. Alternatively the SSCS (service specific convergence sublayer) definition can provide the same function, leaving the CID available as the primary line identity discriminator.

e.g. 1) Mixed use of CID - 7 functions per line of 31 lines maximum
30 CID 0000 0000 .. 0000 0111 reserved for AAL2
CID 0000 1000 .. 000 1111 for line 1 functions 0..7.

CID 11111 1000 .. 0000 1111 for line 2 to 31

In a situation where several network line terminating equipments are colocated the mini-cells carrying EOC messages can be formed into a single AAL2 stream processed by common management functions. Some functions can be processed locally to the line modem functions, such as dealing with remote indicator bits for example, and other minicells onwards routed via a composite AAL2 mini-cell stream. The local functions can then also communicate with separate higher level management functions through the same AAL2 stream.

In a further embodiment, the AAL2 stream can be handled as a naked AAL2 data stream (also known as AL2 protocol) or it can be further encapsulated in ATM. This approach is particularly appropriate for ATM specific equipment which implements an ATM backplane. A single ATM VC is then used for carrying all the traffic from a single module/line-card to a common processing function located with the line cards/modules, for example in a remote optical network unit (ONU) or elsewhere in the network for example at a passive optical network head-end. Where network termination implements narrow-band voice on AAL2, the AAL2 processing blocks can be shared with the EOC channel. This cannot be extended so that if the bandwidth of the EOC channel is expanded then multiple narrow-band ISDN channels and IP/PPP data can be multiplexed with the EOC information on the same bearer channel.

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An alternative method for transporting Narrow-band traffic is by the use of circuit emulation in which an SSCS based ATM AAL1 with time stamps relies on transport of the NTR by the transport system(carried for example as a phase stamp in the synchronisation mini-cell); or by plesiochronous multiplexing of null encapsulated narrow band traffic with autonomous timing recovery. This is illustrated in figure 6a which refers

to synchronous transport and figure 6b which refers to plesiochronous transport. For synchronous transport (figure 6a) the timing byte is a signed integer giving the offset from the NTR phase. It is also possible to

use two bytes each for the NTR phase and user timing phase.

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Where the EOC mini-cell is synchronised, a maximal length pseudo random binary sequence (PRBS) may be added modulo two to the channel data viewed as a bit stream where each byte is transmitted MSB first. This provides scrambling which randomises the data stream to minimise narrow band RFI emissions and to prevent user simulation of valid mini-cell headers. The phase of the added sequence may be determined with the EOC superframe alignment at the receiver. This is described below with reference to figure 7 which is a block schematic flow chart defining the scrambler operation.

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Synchronisation with the receiver is performed by sending null data until a synchronisation state is achieved, after which normal data can be sent over the link. The scheme is dependent on the fact that it is possible to detect an all zero data sequence before scrambling by testing the received sequence with the recurrence relation implied by the PRBS generator polynomial. The basis of the synchronisation scheme is co-operation between the two ends of the link, so that neither the synchronisation mini-cell nor any other mini-cell is transmitted unless the receiver at the same end indicates that its descrambler is in synchronisation. Essentially, the scrambler circuit performs a summation of the current bit sample with a running sum of previous samples occurring at integer multiples of n-bit offsets from the current sample to produce the scrambled output corresponding de-scrambler operates on the received signal to reverse the scrambling process by subtracting the current data sample from the previous data sample offset by the same n-bit positions.

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In the state "Scrambler out of Sync Frame out of sync" the receiver must detect at least one whole frame of null EOC traffic (no sync mini-cells) before entering the "Scrambler in sync" state.

The detection of scrambled all-zeros traffic (null EOC traffic) can be performed by polynomial multiplication utilising the transmit generator polynomial. A conventional feed-forward de-scrambler can perform this multiplication on a continuous basis and will produce continuous stream of all zero data starting N bits after the arrival of the first scrambled zero data bit where N is the order of the scrambler polynomial.

A free running scrambler must in any case be re-synchronised with the supposed scrambled null data sequence by loading that circuit with received data samples and checking that its output (prediction) is the same as the actual received sequence for at least N bits.

If multiplicative checking is used the synchronisation can be performed only once after the scrambled null sequence has been confirmed, but instead the free running scrambler could be designed to continuously attempt synchronisation on different scrambler phases until the predicted and received sequences are the same for the required period.

Since the synchronisation process takes place when there are no user minicells on the link, there is no necessity for separate payload scrambling to prevent user imitation of synchronisation patterns as the minicell contents are ignored while the link is being synchronised. A suitable descrambler synchronisation arrangement is illustrated in figure 8. The construction and operation of this descrambler circuit will be familiar to those skilled in the art. Because scrambled null data is being transmitted during the synchronisation process, the corresponding descrambled sequence will also be a null sequence when synchronisation between transmitter and receiver is achieved. Blocks of

incoming received data are loaded into the descrambler register 81, the switch 82 is then opened and the scrambled sequence is descrambled by the register elements. A counter 83 determines the number of zeros in the register output and, when this count reaches a preset value indicative of synchronisation, enables the descrambling process for actual data.

The effective EOC frame length can be extended by incorporating user bytes, including FEC check bytes, which are ignored when processing or extracting the EOC mini-cells.

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Where only EOC mini-cells are carried, the most significant bits of the CID field may be used to select the line for which the EOC mini-cell is intended. This field is available as an address field for the network terminal which is to be used as the most significant bits of the upstream CID number to be used. This identifies upstream mini-cells as to their source. Alternatively the most significant bits of the CID can be zeroed downstream and set to the line identity at the LTE upstream.

Where the EOC mini-cells are mixed with other AAL2 traffic a special CID range (as allocated for instance to line 1 in the above example) may be used to identify them. In the example CIDs 8 to 15 are used to identify EOC cells, and available CIDs for traffic start at 16.

Where super-framing is performed by the EOC mini-cell, indicator bits are only allowed to be transmitted one per super-frame in the synchronising mini-cell. Therefore the opcode range which indicates presence of indicator bits can be used as a super-frame sync marker. Optionally a register access field can be added to the sync mini-cell payload. At other times EOC mini-cells can contain simple EOC commands, or register access messages.

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To ensure reliable transmission when the link itself is subject to significant bit errors and to minimize the probability of false EOC mini-cell reception, a special protocol may be applied to cells which are identifiable as EOC mini-cells from the CID range. It is assumed that all mini-cells are discarded if the HEC is found to be bad, and where an EOC mini-cell is used for super-frame synchronization.

EOC mini-cells requesting register write are accepted on receipt of a correct CRC and HEC, and are acknowledged by re-transmission backwards. EOC mini-cells requesting register read operation are accepted on receipt of a correct CRC and HEC, or if a time-out period is exceeded, the register access request is sent again with the same message number. The process is repeated until a good response is received, or on a long time out the EOC link failed is declared to the network management layer.

Advantageously, messages are numbered to allow overlapping of requests, although it may be desirable to send only one message at a time, waiting until each message is correctly acknowledged until sending the next. A register number is reserved for mini-cells which are immediately looped back unaltered for EOC path integrity testing.

It will be understood that the above description of preferred embodiments is given by way of example only and that various modifications may be made by those skilled in the art without departing from the spirit and scope of the invention.